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Shumakher, E.; Eisenstein, G.;
Microwave Theory and Techniques, IEEE Transactions on
Volume 52, Issue 5, May 2004 Page(s):1523 - 1537
Digital Object Identifier 10.1109/TMTT.2004.827035

AbstractPlus | Full Text: PDF(1080 KB)   IEEE JNL
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Rosenbaum, E.; Liu, Z.; Hu, C.;
Electron Devices, IEEE Transactions on
Volume 40, Issue 12, Dec. 1993 Page(s):2287 - 2295
Digital Object Identifier 10.1109/16.249477

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Jiang Tao; Cheung, N.W.; Chenming Hu;
Electron Devices, IEEE Transactions on
Volume 41, Issue 4, April 1994 Page(s):539 - 545
Digital Object Identifier 10.1109/16.278507

AbstractPlus | Full Text: PDF(524 KB)   IEEE JNL
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☐     4. **Noise in hydrogenated amorphous silicon**
Johanson, R.E.; Gunes, M.; Kasap, S.O.;
Circuits, Devices and Systems, IEE Proceedings [see also IEE Proceedings G- Circuits, Devices a
Volume 149, Issue 1, Feb. 2002 Page(s):68 - 74
Digital Object Identifier 10.1049/ip-cds:20020333

AbstractPlus | Full Text: PDF(776 KB)   IEE JNL

☐     5. **Performance Simulations and Verification for Power/Ground Planes Connected with Ground Multilayer PCBs**
SeungJoo Lee; HaeJin Hwang; Jun Lee; JongHumn Baek; JongGwan Yook;
Electronic Components and Technology, 2005. ECTC '05. Proceedings
31 May-3 June 2005 Page(s):722 - 726
Digital Object Identifier 10.1109/ECTC.2005.1441349

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**1**   Modeling and simulating electronic textile applications

Thomas Martin, Mark Jones, Joshua Edmison, Tanwir Sheikh, Zahi Nakad
June 2004 **ACM SIGPLAN Notices , Proceedings of the 2004 ACM SIGPLAN/SIGBED conference on Languages, compilers, and tools for embedded systems LCTES '04**, Volume 39 Issue 7
**Publisher:** ACM Press
Full text available: 📄 pdf(421.80 KB)   Additional Information: full citation, abstract, references, index terms

This paper describes our design of a simulation environment for electronic textiles (e-textiles) and our experiences with that environment. This simulation environment, based upon Ptolemy II, enables us to model a diverse range of areas related to the design of electronic textiles, including the physical environment they will be used in, the behavior of the sensors incorporated into the fabric, the on-fabric network, the power consumption of the system, and the execution of the application and s ...

**Keywords**: context awareness, electronic textiles, smart fabrics, wearable computing

**2**   Modeling and simulation of self-similar variable bit rate compressed video: a unified approach

Changcheng Huang, Michael Devetsikiotis, Ioannis Lambadaris, A. Roger Kaye
October 1995 **ACM SIGCOMM Computer Communication Review , Proceedings of the conference on Applications, technologies, architectures, and protocols for computer communication SIGCOMM '95**, Volume 25 Issue 4
**Publisher:** ACM Press
Full text available: 📄 pdf(1.06 MB)   Additional Information: full citation, abstract, references, citings, index terms

Variable bit rate (VBR) compressed video is expected to become one of the major loading factors in high-speed packet networks such as ATM-based B-ISDN. However, recent measurements based on long empirical traces (complete movies) revealed that VBR video traffic possesses *self-similar* (or *fractal*) characteristics, meaning that the dependence in the traffic stream lasts much longer than traditional models can capture.In this paper, we present a unified approach which, in addition to ...

**3**   Software/modelware tutorials a: Non-item based tools: non-item based discrete-event simulation tools

Richard A. Phelps, David J. Parsons, Andrew J. Siprelle
December 2002 **Proceedings of the 34th conference on Winter simulation: exploring new frontiers**
**Publisher:** Winter Simulation Conference
Full text available: 📄 pdf(359.58 KB)    Additional Information: <u>full citation</u>, <u>abstract</u>

Discrete event simulation has traditionally been defined by items (or entities). This modeling paradigm has served the simulation industry well, but falls far short for many industries in which the parts / pieces mindset simply does not accurately portray their particular processes. For the last ten years Simulation Dynamics has been working with industries where the item paradigm falls short as a descriptive tool. This work has led to the development of a revolutionary set of simulation tool ...

**4** An adaptive memory management protocol for Time Warp parallel simulation

Samir R. Das, Richard M. Fujimoto
May 1994 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1994 ACM SIGMETRICS conference on Measurement and modeling of computer systems SIGMETRICS '94**, Volume 22 Issue 1
**Publisher:** ACM Press

Full text available: 📄 pdf(1.13 MB)    Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

It is widely believed that Time Warp is prone to two potential problems: an excessive amount of wasted, rolled back computation resulting from "rollback thrashing" behaviors, and inefficient use of memory, leading to poor performance of virtual memory and/or multiprocessor cache systems. An adaptive mechanism is proposed based on the Cancelback memory management protocol that dynamically controls the amount of memory used in the simulation in order to maximize performance. The p ...

**5** Control Flow Modeling in Statistical Simulation for Accurate and Efficient Processor Design Studies
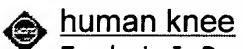
Lieven Eeckhout, Robert H. Bell Jr., Bastiaan Stougie, Koen De Bosschere, Lizy K. John
March 2004 **ACM SIGARCH Computer Architecture News , Proceedings of the 31st annual international symposium on Computer architecture ISCA '04**, Volume 32 Issue 2
**Publisher:** IEEE Computer Society, ACM Press
Full text available: 📄 pdf(228.94 KB)    Additional Information: <u>full citation</u>, <u>abstract</u>

Designing a new microprocessor is extremely time-consuming.One of the contributing reasons is that computerdesigners rely heavily on detailed architectural simulations,which are very time-consuming. Recent workhas focused on statistical simulation to address this issue.The basic idea of statistical simulation is to measurecharacteristics during program execution, generate asynthetic trace with those characteristics and then simulatethe synthetic trace. The statistically generated synthetictrace ...

**6** Interactive simulation of biomechanical systems: The kinematics and stress of the human knee

Frederic I. Parke, Mark Friedell
January 1978 **Proceedings of the 1978 annual conference - Volume 2**
**Publisher:** ACM Press
Full text available: 📄 pdf(418.92 KB)    Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

The application of real-time shaded computer graphics to the visualization of certain biomechanical aspects of the human knee. The published work of various researchers in the biomechanics of the knee is incorporated into a computer model. Interactive techniques are utilized to visualize the dynamics and stress of the knee joint based on this biomechanical model.

Keywords: Biomechanical models, Computer graphics

7 Using speculative retirement and larger instruction windows to narrow the
performance gap between memory consistency models
Parthasarathy Ranganathan, Vijay S. Pai, Sarita V. Adve
June 1997 **Proceedings of the ninth annual ACM symposium on Parallel algorithms
and architectures**
**Publisher:** ACM Press
Full text available: pdf(1.83 MB)   Additional Information: full citation, references, citings, index terms

8 On the analytical modeling of database concurrency control
Philip S. Yu, Daniel M. Dias, Stephen S. Lavenberg
September 1993 **Journal of the ACM (JACM)**, Volume 40 Issue 4
**Publisher:** ACM Press
Full text available: pdf(2.75 MB)   Additional Information: full citation, abstract, references, citings, index terms

The Concurrency Control (CC) scheme employed can profoundly affect the performance of transaction-processing systems. In this paper, a simple unified approximate analysis methodology to model the effect on system performance of data contention under different CC schemes and for different system structures is developed. This paper concentrates on modeling data contention and then, as others have done in other papers, the solutions of the data contention model are coupled with a standard hard ...

9 Use of an amputee-computer interactive facility in above-knee prosthesis research
Woodie C. Flowers
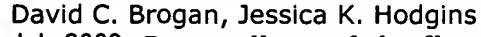January 1974 **Proceedings of the 1974 annual conference**
**Publisher:** ACM Press
Full text available: pdf(698.46 KB)   Additional Information: full citation, abstract, references, index terms

Significant contributions to the development of improved knee flexion control mechanisms for above-knee prostheses can be made through evaluation of existing computer-developed performance criteria and through evaluation of schemes for providing the amputee with voluntary knee torque control. A new amputee-computer interactive technique and facility has been developed for conducting this research. This new technique allows the evaluation of proposed swing and stance control schemes and prop ...

Keywords: Above-knee prostheses, Man-interactive simulation

10 Session 2B: multiagent simulation: Simulation level of detail for multiagent control
David C. Brogan, Jessica K. Hodgins
July 2002 **Proceedings of the first international joint conference on Autonomous
agents and multiagent systems: part 1**
**Publisher:** ACM Press
Full text available: pdf(348.39 KB)   Additional Information: full citation, abstract, references, citings, index terms

Many classes of applications require multiagent navigation control algorithms to specify the movements and actions of heterogeneous groups containing thousands of characters. The scale and complexity of these interacting character groups require navigation control algorithms that are both generalizable and specifically tuned to particular character platforms. We propose a technique called simulation level of detail (LOD) that provides a simulation-based interface between navigation control algor ...

**Keywords**: mobile agents, multiagent simulation, path planning

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**11**  Ladder queue: An O(1) priority queue structure for large-scale discrete event simulation

Wai Teng Tang, Rick Siow Mong Goh, Ian Li-Jin Thng

July 2005  **ACM Transactions on Modeling and Computer Simulation (TOMACS)**, Volume 15 Issue 3

**Publisher**: ACM Press

Full text available: pdf(2.51 MB)    Additional Information: full citation, abstract, references, index terms

This article describes a new priority queue implementation for managing the pending event set in discrete event simulation. Extensive empirical results demonstrate that it consistently outperforms other current popular candidates. This new implementation, called Ladder Queue, is also theoretically justified to have $O(1)$ amortized access time complexity, as long as the mean *jump* parameter of the priority increment distribution is finite and greater than zero, regardless of its varia ...

**Keywords**: Pending event set implementations, calendar queue, priority queue

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**12**  Buffered tree construction: Modeling of coplanar waveguide for buffered clock tree

Jun Chen, Lei He

January 2004  **Proceedings of the 2004 conference on Asia South Pacific design automation: electronic design and solution fair ASP-DAC '04 , Proceedings of the 2004 conference on Asia South Pacific design automation: electronic design and solution fair ASP-DAC '04**

**Publisher**: IEEE Press , IEEE Press

Full text available: pdf(282.72 KB)
Publisher Site    Additional Information: full citation, abstract, references

Owing to inductive effect, coplanar waveguide (CPW) is widely used to achieve signal integrity in high performance clock designs. In this paper, we first propose a piece-wise linear (PWL) model for the far-end response of a CPW considering ramp input and capacitive loading. The PWL model has a high accuracy but uses at least 1000x less time compared to SPICE. We then apply the PWL model to synthesize the CPW geometry for clock trees considering constrains of rising time and oscillation at sinks. ...

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**13**  Adapting simulated behaviors for new characters

Jessica K. Hodgins, Nancy S. Pollard

August 1997  **Proceedings of the 24th annual conference on Computer graphics and interactive techniques**

**Publisher**: ACM Press/Addison-Wesley Publishing Co.

Full text available: pdf(310.16 KB)    Additional Information: full citation, references, citings, index terms

**Keywords**: dynamic simulation, human motion, motion control, simulated annealing

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**14**  Adaptive memory management and optimism control in time warp

Samir R. Das, Richard M. Fujimoto

April 1997  **ACM Transactions on Modeling and Computer Simulation (TOMACS)**, Volume 7 Issue 2

**Publisher**: ACM Press

Additional Information:

Full text available: 🗎 pdf(321.66 KB)                    <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>, <u>review</u>

It is widely believed that the Time Warp protocol for parallel discrete event simulation is prone to two potential problems: an excessive amount of wasted, rolled back computation resulting from "rollback thrashing" behaviors, and inefficient use of memory, leading to poor performance of virtual memory and/or multiprocessor cache systems. An adaptive mechanism is proposed based on the Cancelback memory management protocol for shared-memory multiprocessors that dynamically contro ...

**15** <u>Semiconductor manufacturing: Cycle time versus throughput analysis: an overall framework for generating simulation-based cycle time-throughput curves</u>
Sungmin Park, Gerald T. Mackulak, John W. Fowler
December 2001 **Proceedings of the 33nd conference on Winter simulation**
**Publisher:** IEEE Computer Society
Full text available: 🗎 pdf(382.34 KB)    Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

A simulation-based cycle time-throughput curve requires a large amount of simulation output data, and an experimentation framework is needed to enhance the precision and accuracy of a simulation-based cycle time-throughput curve. In this research, approaches and solutions are presented on three prime issues: 1) the establishment of the simulation sampling strategies; 2) the determination of the simulation sequences; and 3) the determination of the length of a simulation run. First, strategic sim ...

**16** <u>Stochastic modeling of TCP in networks with abrupt delay variations</u>
Alhussein A. Abouzeid, Sumit Roy
September 2003 **Wireless Networks**, Volume 9 Issue 5
**Publisher:** Kluwer Academic Publishers
Full text available: 🗎 pdf(362.31 KB)    Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

An analytical model of TCP (Transport Control Protocol) over an end-to-end path with random abrupt round-trip time (RTT) changes is presented. Modeling the RTT as a stochastic process, we analytically quantify and compare between the degree of degradation of the steady-state average throughput and window size due to spurious retransmissions for the different versions of TCP (Reno/New Reno versus Tahoe). The modeling methodology in this paper is used for evaluating different design alternatives f ...

**Keywords**: mobile ad-hoc networks, performance analysis, satellite networks, transport control protocol, wireless networks

**17** <u>Animating human athletics</u>
Jessica K. Hodgins, Wayne L. Wooten, David C. Brogan, James F. O'Brien
September 1995 **Proceedings of the 22nd annual conference on Computer graphics and interactive techniques**
**Publisher:** ACM Press
Full text available: 🗎 pdf(412.08 KB)    Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

**Keywords**: computer animation, dynamic simulation, human motion, motion control, physically realistic modeling

**18** <u>Theoretical modeling of superscalar processor performance</u>
Derek B. Noonburg, John P. Shen

November 1994 **Proceedings of the 27th annual international symposium on Microarchitecture**
**Publisher:** ACM Press

Full text available: 📄 pdf(1.06 MB)      Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

The current trace-driven simulation approach to determine superscalar processor performance is widely used but has some shortcomings. Modern benchmarks generate extremely long traces, resulting in problems with data storage, as well as very long simulation runtimes. More fundamentally, simulation generally does not provide significant insight into the factors that determine performance or a characterization of their interactions. This paper proposes a theoretical model of superscalar proces ...

**19** <u>An analytical cache model</u>

A. Agarwal, J. Hennessy, M. Horowitz
May 1989 **ACM Transactions on Computer Systems (TOCS)**, Volume 7 Issue 2
**Publisher:** ACM Press

Full text available: 📄 pdf(2.51 MB)      Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>, <u>review</u>

Trace-driven simulation and hardware measurement are the techniques most often used to obtain accurate performance figures for caches. The former requires a large amount of simulation time to evaluate each cache configuration while the latter is restricted to measurements of existing caches. An analytical cache model that uses parameters extracted from address traces of programs can efficiently provide estimates of cache performance and show the effects of varying cache parameters. By repre ...

**20** <u>Efficient distributed simulation</u>

Vijay Madisetti, Jean Walrand, David Messerschmitt
March 1989 **Proceedings of the 22nd annual symposium on Simulation ANSS '89**
**Publisher:** IEEE Computer Society Press

Full text available: 📄 pdf(1.46 MB)      Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

Discrete-event systems are used to model a number of engineering applications ranging from performance analysis of large scale communication networks, computer-aided-design (CAD) of circuits to simulation of manufacturing systems. Except for a small set, these systems are analytically intractable and in addition prohibitive to evaluate numerically. Simulation of such complex systems is exceedingly slow to run (and also to develop). Therefore, the development of simulation speedup methods is ...

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**1**  The effect of state-saving in optimistic simulation on a cache-coherent non-uniform memor architecture
Christopher D. Carothers, Kalyan S. Perumalla, Richard M. Fujimoto
December 1999 **Proceedings of the 31st conference on Winter simulation: Simulation---a b⃒ the future - Volume 2**
**Publisher:** ACM Press
Full text available: 🗇 pdf(84.70 KB)            Additional Information: full citation, references, citings, index terms

**2**  Forward error control for MPEG-2 video transport in a wireless ATM LAN
Ender Ayanoglu, Pramod Pancha, Amy R. Reibman, Shilpa Talwar
December 1996 **Mobile Networks and Applications**, Volume 1 Issue 3
**Publisher:** Kluwer Academic Publishers
Full text available: 🗇 pdf(439.61 KB)            Additional Information: full citation, abstract, references, citings, index ter⃒

The possibility of providing multimedia services to mobile users has led to interest in designing ⃒ wireless networks that can guarantee quality of service for traffic flows. However, a fundamenta in these networks is that severe losses may occur due to the random fading characteristics of th channel. Error control algorithms which compensate for these losses are required in order to ach reasonable loss rates. In this paper, the performance of error control ba ...

**3**  Adaptive memory management and optimism control in time warp
Samir R. Das, Richard M. Fujimoto
April 1997   **ACM Transactions on Modeling and Computer Simulation (TOMACS)**, Volume 7 Is⃒
**Publisher:** ACM Press
Full text available: 🗇 pdf(321.66 KB)            Additional Information: full citation, abstract, references, citings, index ter⃒

It is widely believed that the Time Warp protocol for parallel discrete event simulation is prone t⃒ potential problems: an excessive amount of wasted, rolled back computation resulting from "roll thrashing" behaviors, and inefficient use of memory, leading to poor performance of virtual me⃒ multiprocessor cache systems. An adaptive mechanism is proposed based on the Cancelback m⃒ management protocol for shared-memory multiprocessors that dynamically contro ...

**4**  The SPLASH-2 programs: characterization and methodological considerations
Steven Cameron Woo, Moriyoshi Ohara, Evan Torrie, Jaswinder Pal Singh, Anoop Gupta

May 1995  **ACM SIGARCH Computer Architecture News , Proceedings of the 22nd annual international symposium on Computer architecture ISCA '95**, Volume 23 Issue 2
**Publisher:** ACM Press
Full text available: pdf(1.73 MB)          Additional Information: full citation, abstract, references, citings, index terr

The SPLASH-2 suite of parallel applications has recently been released to facilitate the study of c and distributed shared-address-space multiprocessors. In this context, this paper has two goals. quantitatively characterize the SPLASH-2 programs in terms of fundamental properties and arch interactions that are important to understand them well. The properties we study include the computational load balance, communication to computation ratio and traffic needs, impor ...

5  Implications of hierarchical N-body methods for multiprocessor architectures

Jaswinder Pal Singh, John L. Hennessy, Anoop Gupta
May 1995  **ACM Transactions on Computer Systems (TOCS)**, Volume 13 Issue 2
**Publisher:** ACM Press
Full text available: pdf(4.66 MB)          Additional Information: full citation, abstract, references, citings, index terr

To design effective large-scale multiprocessors, designers need to understand the characteristics applications that will use the machines. Application characteristics of particular interest include t of communication relative to computation, the structure of the communication, and the local cac memory requirements, as well as how these characteristics scale with larger problems and mack important class of applications is based on hierarchical N-body methods, w ...

**Keywords:** N-body methods, communication abstractions, locality, message passing, parallel a| parallel computer architecture, scaling, shared address space, shared memory

6  Modeling and simulation of self-similar variable bit rate compressed video: a unified appro;

Changcheng Huang, Michael Devetsikiotis, Ioannis Lambadaris, A. Roger Kaye
October 1995 **ACM SIGCOMM Computer Communication Review , Proceedings of the confer Applications, technologies, architectures, and protocols for computer commu SIGCOMM '95**, Volume 25 Issue 4
**Publisher:** ACM Press
Full text available: pdf(1.06 MB)          Additional Information: full citation, abstract, references, citings, index terr

Variable bit rate (VBR) compressed video is expected to become one of the major loading factor: speed packet networks such as ATM-based B-ISDN. However, recent measurements based on lo empirical traces (complete movies) revealed that VBR video traffic possesses *self-similar* (or fra( characteristics, meaning that the dependence in the traffic stream lasts much longer than traditi models can capture.In this paper, we present a unified approach which, in addition to ...

7  Designing human-computer interfaces for quadriplegic people

Constantine E. Steriadis, Philip Constantinou
June 2003  **ACM Transactions on Computer-Human Interaction (TOCHI)**, Volume 10 Issue 2
**Publisher:** ACM Press
Full text available: pdf(1.20 MB)          Additional Information: full citation, abstract, references, citings, index terr

The need for participation in an emerging *Information Society* has led to several research efforts designing accessibility solutions for disabled people. In this paper we present a method for deve Human-Computer Interfaces (HCIs) for quadriplegic people in modern programming environmer presented method accommodates the design of scanning interfaces with modern programming t leading to flexible interfaces with improved appearance and it is based on the use of specially ...

**Keywords:** Accessibility, assistive technology, augmentative communications, disability, graphi( keyboard, motor-impaired users, mouse simulation, quadriplegic people, scanning selection, sin( input, wifsid, word-prediction

**8**  The elements of nature: interactive and realistic techniques

Oliver Deusen, David S. Ebert, Ron Fedkiw, F. Kenton Musgrave, Przemyslaw Prusinkiewicz, Doug I Stam, Jerry Tessendorf

August 2004 **Proceedings of the conference on SIGGRAPH 2004 course notes GRAPH '04**

Publisher: ACM Press

Full text available: pdf(17.65 MB)          Additional Information: full citation, abstract

> This updated course on simulating natural phenomena will cover the latest research and product techniques for simulating most of the elements of nature. The presenters will provide movie proc interactive simulation, and research perspectives on the difficult task of photorealistic modeling, and animation of natural phenomena. The course offers a nice balance of the latest interactive g hardware-based simulation techniques and the latest physics-based simulation techni ...

**9**  Level set and PDE methods for computer graphics

David Breen, Ron Fedkiw, Ken Museth, Stanley Osher, Guillermo Sapiro, Ross Whitaker

August 2004 **Proceedings of the conference on SIGGRAPH 2004 course notes GRAPH '04**

Publisher: ACM Press

Full text available: pdf(17.07 MB)          Additional Information: full citation, abstract

> Level set methods, an important class of partial differential equation (PDE) methods, define dyni surfaces implicitly as the level set (iso-surface) of a sampled, evolving nD function. The course t preparatory material that introduces the concept of using partial differential equations to solve p computer graphics, geometric modeling and computer vision. This will include the structure and of several different types of differential equations, e.g. the level set eq ...

**10**  LocusRoute: a parallel global router for standard cells

Jonathan Rose

June 1988    **Proceedings of the 25th ACM/IEEE conference on Design automation**

Publisher: IEEE Computer Society Press

Full text available: pdf(805.58 KB)          Additional Information: full citation, abstract, references, citings, index ter

> A fast and easily parallelizable global routing algorithm for standard cells and its parallel implem presented. LocusRoute is meant to be used as the cost function for a placement algorithm and s context constrains the structure of the global routing algorithm and its parallel implementation. is based on enumerating a subset of all two-bend routes between two points, and results in 16% fewer total number of tracks than the TimberWolf global router for stan ...

**11**  A study of the comparative effects of various means of motion cueing during a simulated compensatory tracking task

Burnell T. McKissick, Billy R. Ashworth, Russell V. Parrish, Dennis J. Martin

January 1980 **Proceedings of the 13th annual symposium on Simulation**

Publisher: IEEE Press

Full text available: pdf(2.02 MB)          Additional Information: full citation, abstract, references, index terms

> NASA's Langley Research Center conducted a simulation experiment to ascertain the comparativ of motion cues (combinations of platform motion and g-seat normal acceleration cues) on comp tracking performance. In the experiment, a full six-degree-of-freedom YF-16 model was used as simulated pursuit aircraft. The Langley Visual Motion Simulator (with in-house developed wash-c Langley developed g-seat were principal components of the simulation. The results of the ...

**12**  Software/modelware tutorials a: Non-item based tools: non-item based discrete-event simu tools

Richard A. Phelps, David J. Parsons, Andrew J. Siprelle

December 2002 **Proceedings of the 34th conference on Winter simulation: exploring new fr**

**Publisher:** Winter Simulation Conference

Full text available: 📄 pdf(359.58 KB)          Additional Information: full citation, abstract

Discrete event simulation has traditionally been defined by items (or entities). This modeling par served the simulation industry well, but falls far short for many industries in which the parts / pi mindset simply does not accurately portray their particular processes. For the last ten years Sim Dynamics has been working with industries where the item paradigm falls short as a descriptive work has led to the development of a revolutionary set of simulation tool ...

**13** Application and evaluation of large deviation techniques for traffic engineering in broadban networks

Costas Courcoubetis, Vasilios A. Siris, George D. Stamoulis

June 1998   **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1998 A SIGMETRICS joint international conference on Measurement and modeling of systems SIGMETRICS '98/PERFORMANCE '98**, Volume 26 Issue 1

**Publisher:** ACM Press

Full text available: 📄 pdf(1.41 MB)          Additional Information: full citation, abstract, references, citings, index ter

Accurate yet simple methods for traffic engineering are important for efficient dimensioning of b networks. The goal of this paper is to apply and evaluate large deviation techniques for traffic er In particular, we employ the recently developed theory of *effective bandwidths*, where the effect bandwidth depends not only on the statistical characteristics of the traffic stream, but also on a l operating point through two parameters, the *space* and *time*

**Keywords:** ATM, broadband networks, effective bandwidths, large deviations, traffic engineerin

**14** Session 5B: Embedded tutorial: CAD solutions and outstanding challenges for mixed-sign; IC design: CAD solutions and outstanding challenges for mixed-signal and RFIC design

Domine Leenaerts, Georges Gielen, Rob A. Rutenbar

November 2001   **Proceedings of the 2001 IEEE/ACM international conference on Computer-, design**

**Publisher:** IEEE Press

Full text available: 📄 pdf(1.87 MB)          Additional Information: full citation, abstract, references, index terms

This tutorial paper addresses the problems and solutions that are posed by the design of mixed-integrated systems on chip (SoC). These include problems in mixed-signal design methodologie flows, problems in analog design productivity, as well as open problems in analog, mixed-signal design, modeling and verification tools. The tutorial explains the problems that are posed by the signal/RF SoC designs, describes the solutions and their underlying methods that exist toda ...

**15** Extended performance evaluation of PRADOS: a scheduling algorithm for traffic integratior wireless ATM network

G. Colombo, L. Lenzini, E. Mingozzi, B. Cornaglia, R. Santaniello

March 2002   **Wireless Networks**, Volume 8 Issue 2/3

**Publisher:** Kluwer Academic Publishers

Full text available: 📄 pdf(231.85 KB)          Additional Information: full citation, abstract, references, index terms

The MAC protocol, known as MASCARA (Mobile Access Scheme based on Contention And Reserv; ATM), is an infrastructure-based, adaptive TDMA scheme, which combines reservation- and cont based access methods to provide multiple access efficiency and Quality-of-Service (QoS) guaran wireless ATM terminal connections that share a common radio channel. Radio channel sharing is by the PRADOS (Prioritized Regulated Allocation Delay Oriented Scheduling) algorithm. In this p;

**Keywords:** Quality-of-Service, WLAN, packet scheduling, service integration, wireless LAN

**16**

## Modeling communication in parallel algorithms: a fruitful interaction between theory and sy

Jaswinder Pal Singh, Edward Rothberg, Anoop Gupta
August 1994 **Proceedings of the sixth annual ACM symposium on Parallel algorithms and architectures**
**Publisher:** ACM Press
Full text available: pdf(1.38 MB)          Additional Information: full citation, abstract, references, citings, index ten

Recently, several theoretical models of parallel architectures have been proposed to replace the the model that is presented to an algorithm designer. A primary focus of the new models is to in cost of interprocessor communication, which is increasingly important in modern parallel archite argue that modeling the communication costs in the architecture or system is only one part of th problem. The other, and usually much more difficult, part is modeling the commu ...

## 17 A simple bandwidth management strategy based on measurements of instantaneous virtua utilization in ATM networks

Kohei Shiomoto, Shinichiro Chaki, Naoaki Yamanaka
October 1998 **IEEE/ACM Transactions on Networking (TON)**, Volume 6 Issue 5
**Publisher:** IEEE Press
Full text available: pdf(267.08 KB)          Additional Information: full citation, references, citings, index terms

**Keywords:** ATM, admission control, low-pass filter, measurement

## 18 Buffer management in shared-memory Time Warp systems

Richard M. Fujimoto, Kiran S. Panesar
July 1995     **ACM SIGSIM Simulation Digest , Proceedings of the ninth workshop on Paralle distributed simulation PADS '95**, Volume 25 Issue 1
**Publisher:** IEEE Computer Society, ACM Press
Full text available: pdf(1.39 MB)          Additional Information: full citation, abstract, references, citings, index ten
Publisher Site

Mechanisms for managing message buffers in Time Warp parallel simulations executing on cache shared-memory multiprocessors are studied. Two simple buffer management strategies called th pool and receiver pool mechanisms are examined with respect to their efficiency, and in particula interaction with multiprocessor cache-coherence protocols. Measurements of implementations or Square Research KSR-2 machine using both synthetic workloads and benchmark applica ...

**Keywords:** Kendall Square Research KSR-2 machine, buffer management, buffer storage, cach shared-memory multiprocessors, discrete event simulation, mall-granularity parallel simulation applications, message buffer memory, message buffers, message passing, multiprocessing prog multiprocessor cache-coherence protocols, multiprocessor-based parallel simulators, partitioned approach, partitioned pool mechanism, receiver pool, sender pool, severe performance degradat shared memory systems, shared-memory time warp systems, storage management, time warp

## 19 Simulating medical decision trees with random variable parameters

Robert S. Dittus, Robert W. Klein
December 1992 **Proceedings of the 24th conference on Winter simulation**
**Publisher:** ACM Press
Full text available: pdf(754.68 KB)          Additional Information: full citation, references, index terms

## 20 Working sets, cache sizes, and node granularity issues for large-scale multiprocessors

Edward Rothberg, Jaswinder Pal Singh, Anoop Gupta
May 1993    **ACM SIGARCH Computer Architecture News , Proceedings of the 20th annual
international symposium on Computer architecture ISCA '93**, Volume 21 Issue 2
**Publisher:** ACM Press
Full text available: pdf(1.58 MB)                Additional Information: full citation, abstract, references, citings, index ter

The distribution of resources among processors, memory and caches is a crucial question faced I
designers of large-scale parallel machines. If a machine is to solve problems with a certain data
should it be built with a large number of processors each with a small amount of memory, or a s
number of processors each with a large amount of memory? How much cache memory should b
per processor for cost-effectiveness? And how do these decisions change as larger problems ...

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1. ☐ **Performance of optimized $Hg_{1-x}Cd_xTe$ long wavelength infrared photoconductors** •
ARTICLE
*Infrared Physics & Technology, Volume 35, Issue 5, August 1994, Pages 661-671*
J. F. Siliquini, C. A. Musca, B. D. Nener and L. Faraone
Abstract

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1. ☐ **Influence of solvent nature on the electrochemical parameters of electrical double layer capacitors • ARTICLE**
*Journal of Electroanalytical Chemistry, Volume 562, Issue 1, 15 January 2004, Pages 33-42*
E. Lust , A. Jänes and M. Arulepp
SummaryPlus | Full Text + Links | PDF (600 K)

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2. ☐ **Toward an integrated continuum model of cerebral dynamics: the cerebral rhythms, synchronous oscillation and cortical stability • ARTICLE**
*Biosystems, Volume 63, Issues 1-3, November-December 2001, Pages 71-88*
J. J. Wright, P. A. Robinson, C. J. Rennie, E. Gordon, P. D. Bourke, C. L. Chapman, N Hawthorn, G. J. Lees and D. Alexander
SummaryPlus | Full Text + Links | PDF (668 K)

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3. ☐ **Direct-coupled high-$T_c$ thin film SQUIDs using step-edge weak-link junctions • ARTICLE**
*Applied Superconductivity, Volume 3, Issues 7-10, July-October 1995, Pages 425-441*
J. Z. Sun, W. J. Gallagher and R. H. Koch
Abstract | Abstract + References | PDF (1621 K)

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4. ☐ **Forward and reverse transduction at the limit of sensitivity studied by correlating electrical and mechanical fluctuations in frog saccular hair cells • ARTICLE**
*Hearing Research, Volume 60, Issue 1, June 1992, Pages 89-102*
Winfried Denk and Watt W. Webb
Abstract

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| ☐ | L5 | model$ and simulat$ and (knee frequenc$ with wavelength) | 1 |
| ☐ | L4 | model$ and simulat$ and cell? and knee frequenc$ | 1 |
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L1: Entry 1 of 1                                File: PGPB                          Mar 2, 2006

PGPUB-DOCUMENT-NUMBER: 20060047490
PGPUB-FILING-TYPE:
DOCUMENT-IDENTIFIER: US 20060047490 A1

TITLE: HIERARCHICAL METHOD OF POWER SUPPLY NOISE AND SIGNAL INTEGRITY ANALYSIS

PUBLICATION-DATE: March 2, 2006

INVENTOR-INFORMATION:

| NAME | CITY | STATE | COUNTRY |
|------|------|-------|---------|
| Breiland; Erik | Colchester | VT | US |
| Budell; Timothy W. | Milton | VT | US |
| Chiu; Charles S. | Essex Junction | VT | US |
| Clouser; Paul L. | Williston | VT | US |
| Erdelyi; Charles K. | Essex Junction | VT | US |
| Welch; Brian P. | Scotia | NY | US |

US-CL-CURRENT: 703/14

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

| Term | Documents |
|------|-----------|
| BREILAND | 18 |
| BREILANDS | 0 |
| POWER | 1581740 |
| POWERS | 98844 |
| SUPPLY | 1178583 |
| SUPPLIES | 438471 |
| SUPPLYS | 340 |
| NOISE | 466739 |
| NOISES | 43974 |

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☐  1.  Document ID:  US 20060047490 A1

L2: Entry 1 of 3        File: PGPB        Mar 2, 2006

PGPUB-DOCUMENT-NUMBER: 20060047490
PGPUB-FILING-TYPE:
DOCUMENT-IDENTIFIER: US 20060047490 A1

TITLE: HIERARCHICAL METHOD OF POWER SUPPLY NOISE AND SIGNAL INTEGRITY ANALYSIS

PUBLICATION-DATE: March 2, 2006

INVENTOR-INFORMATION:

| NAME | CITY | STATE | COUNTRY |
|---|---|---|---|
| Breiland; Erik | Colchester | VT | US |
| Budell; Timothy W. | Milton | VT | US |
| Chiu; Charles S. | Essex Junction | VT | US |
| Clouser; Paul L. | Williston | VT | US |
| Erdelyi; Charles K. | Essex Junction | VT | US |
| Welch; Brian P. | Scotia | NY | US |

US-CL-CURRENT: 703/14

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐  2.  Document ID:  US 20030061571 A1

L2: Entry 2 of 3        File: PGPB        Mar 27, 2003

PGPUB-DOCUMENT-NUMBER: 20030061571
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030061571 A1

TITLE: METHOD OF DESIGNING A VOLTAGE PARTITIONED SOLDER-BUMP PACKAGE

PUBLICATION-DATE: March 27, 2003

INVENTOR-INFORMATION:

| NAME | CITY | STATE | COUNTRY |
|---|---|---|---|
| Buffet, Patrick H. | Essex Junction | VT | US |
| Chiu, Charles S. | Essex Junction | VT | US |
| Sun, Yu H. | Beaverton | OR | US |

US-CL-CURRENT: <u>716</u>/<u>1</u>; <u>716</u>/<u>2</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

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☐   3.   Document ID:  US 6584596 B2

L2: Entry 3 of 3                          File: USPT                     Jun 24, 2003

US-PAT-NO: 6584596
DOCUMENT-IDENTIFIER: US 6584596 B2

TITLE: Method of designing a voltage partitioned solder-bump package

DATE-ISSUED: June 24, 2003

INVENTOR-INFORMATION:
| NAME | CITY | STATE | ZIP CODE | COUNTRY |
|------|------|-------|----------|---------|
| Buffet; Patrick H. | Essex Junction | VT | | |
| Chiu; Charles S. | Essex Junction | VT | | |
| Sun; Yu H. | Williston | VT | | |

US-CL-CURRENT: <u>716</u>/<u>1</u>; <u>703</u>/<u>2</u>, <u>716</u>/<u>10</u>, <u>716</u>/<u>4</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

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| Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS |

| Term | Documents |
|------|-----------|
| POWER | 1581740 |
| POWERS | 98844 |
| SUPPLY | 1178583 |
| SUPPLIES | 438471 |
| SUPPLYS | 340 |
| NOISE | 466739 |
| NOISES | 43974 |
| NOIZE | 79 |
| NOIZES | 2 |
| KNEE | 36016 |
| KNEES | 12765 |
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PGPUB-DOCUMENT-NUMBER: 20060047490
PGPUB-FILING-TYPE:
DOCUMENT-IDENTIFIER: US 20060047490 A1

TITLE: HIERARCHICAL METHOD OF POWER SUPPLY NOISE AND SIGNAL INTEGRITY ANALYSIS

PUBLICATION-DATE: March 2, 2006

INVENTOR-INFORMATION:

| NAME | CITY | STATE | COUNTRY |
|------|------|-------|---------|
| Breiland; Erik | Colchester | VT | US |
| Budell; Timothy W. | Milton | VT | US |
| Chiu; Charles S. | Essex Junction | VT | US |
| Clouser; Paul L. | Williston | VT | US |
| Erdelyi; Charles K. | Essex Junction | VT | US |
| Welch; Brian P. | Scotia | NY | US |

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| Term | Documents |
|------|-----------|
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| CIRCUITS | 743041 |
| KNEE | 36016 |
| KNEES | 12765 |
| MODEL$ | 0 |
| MODEL | 588740 |
| MODELA | 21 |
| MODELAA | 3 |
| MODELABBRV | 1 |